

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	<i>Complete if Known</i>	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Narendra, Siva
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1		Attorney Docket No: 884.575US2

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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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DM		NARENDRA, S , et al., "Sub-1 V Process-Compensated MOS Current Generation Without Voltage Reference", <u>IEEE Symposium on VLSI Circuits Digest of Technology Papers</u> , (2001), pp. 143-144	

EXAMINER	DATE CONSIDERED	1/20/05
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